

# Flight Ready On-Board Processing (OBP)

Re-Programmable Platform



## CAPABILITIES

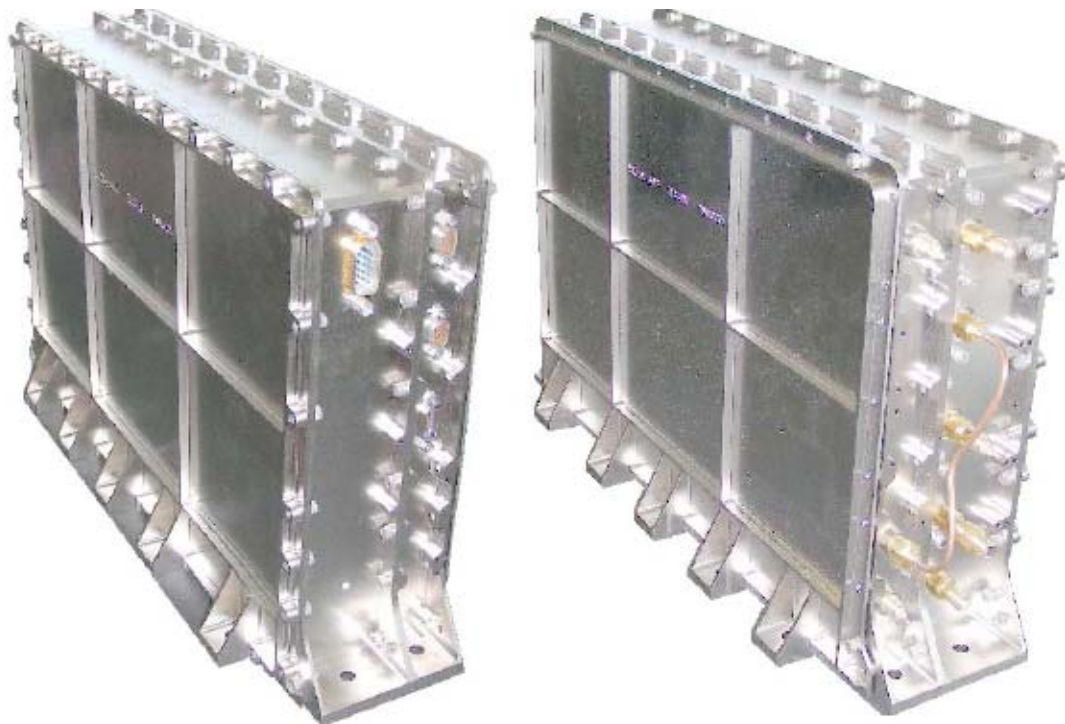
- User Programmable Xilinx FPGA XQ2V6000
- Uploadable image file into EEPROM via UART/CANBUS
- Development tools for Xilinx bit file transfer
- Xilinx Configuration memory scrubbing via Rad Tolerant ACTEL FPGA
- LVDS serial links, 3.8 Gbps upload, 400 Mbps download
- CAN BUS access at 1 Mbps
- Space provided for an RF front end
- Packaged in a stackable chassis (Commercial or Space qualified version), with Analog and Digital EPCs

## ENVIRONMENT

- Designed for GEO orbits, LEO Polar orbits
- Available in commercial or space qualified versions
- Heritage (R & D, analysis, environmental testing)

## TARGET APPLICATIONS

- High speed analog sampling and Digital Signal Processing
- Image signal processing, video encoding – decoding, video compression, DSP, communication
- RF/analog signal processing, demodulation
- Generic Military or Space applications
- Radiation test bed with Scrubbing capability



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